

FIG.1

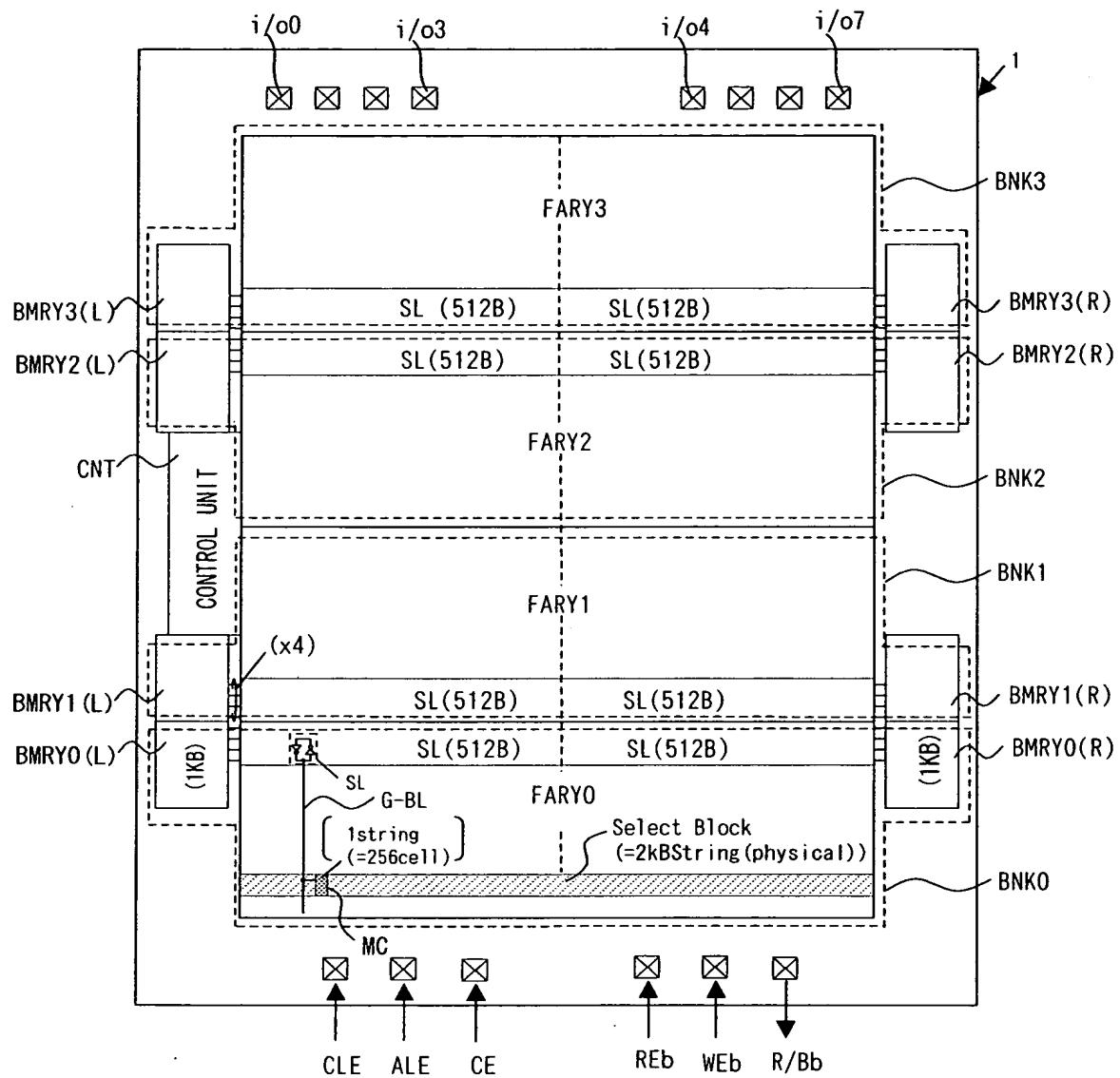
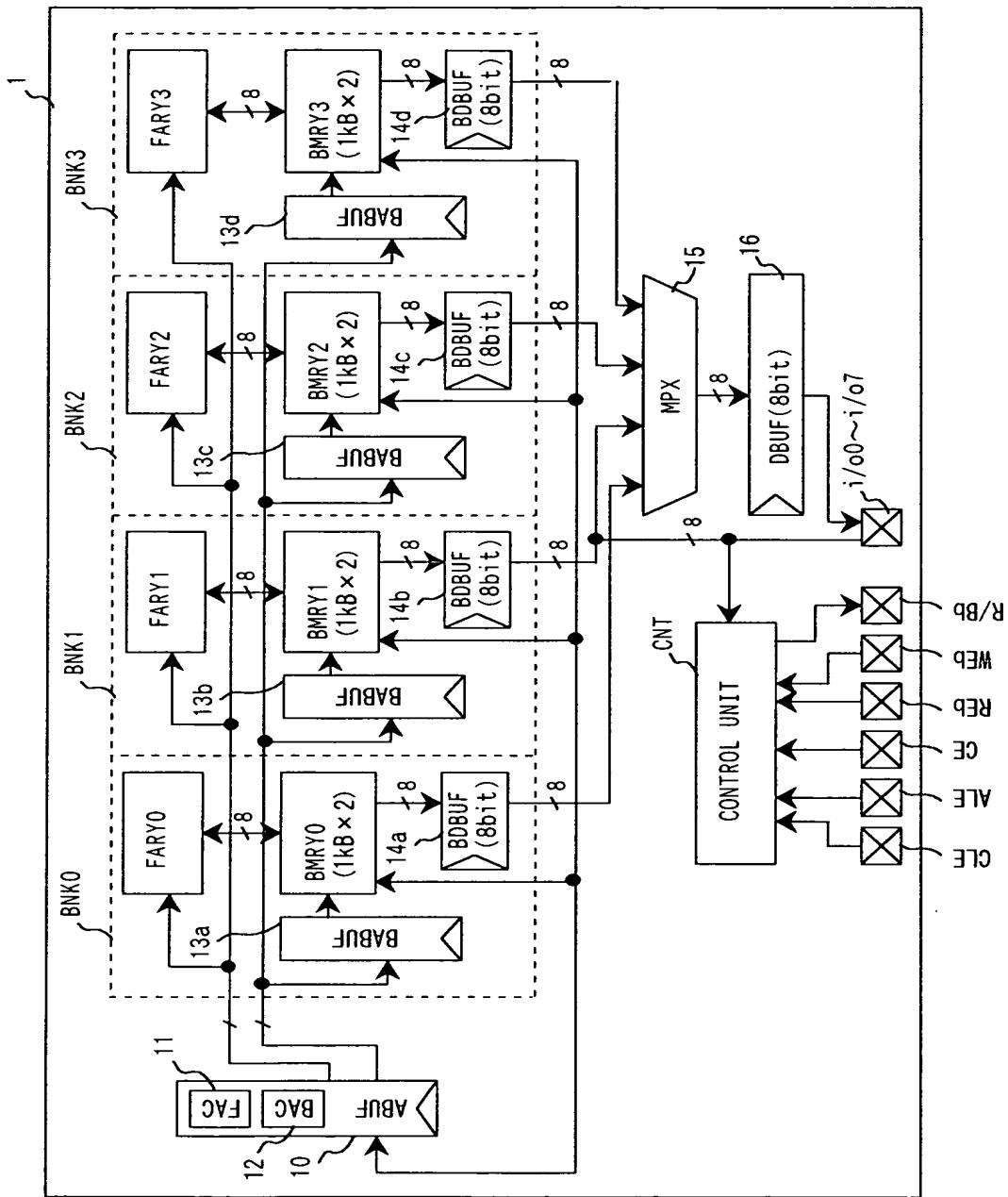


FIG.2



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FIG.3

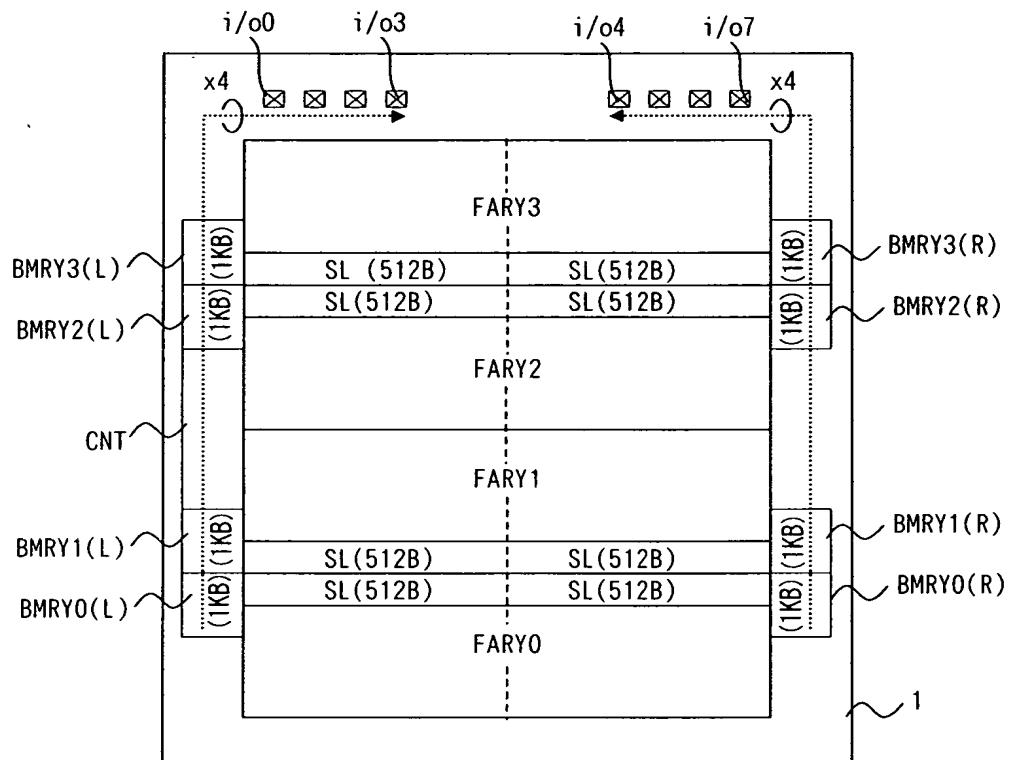


FIG.4

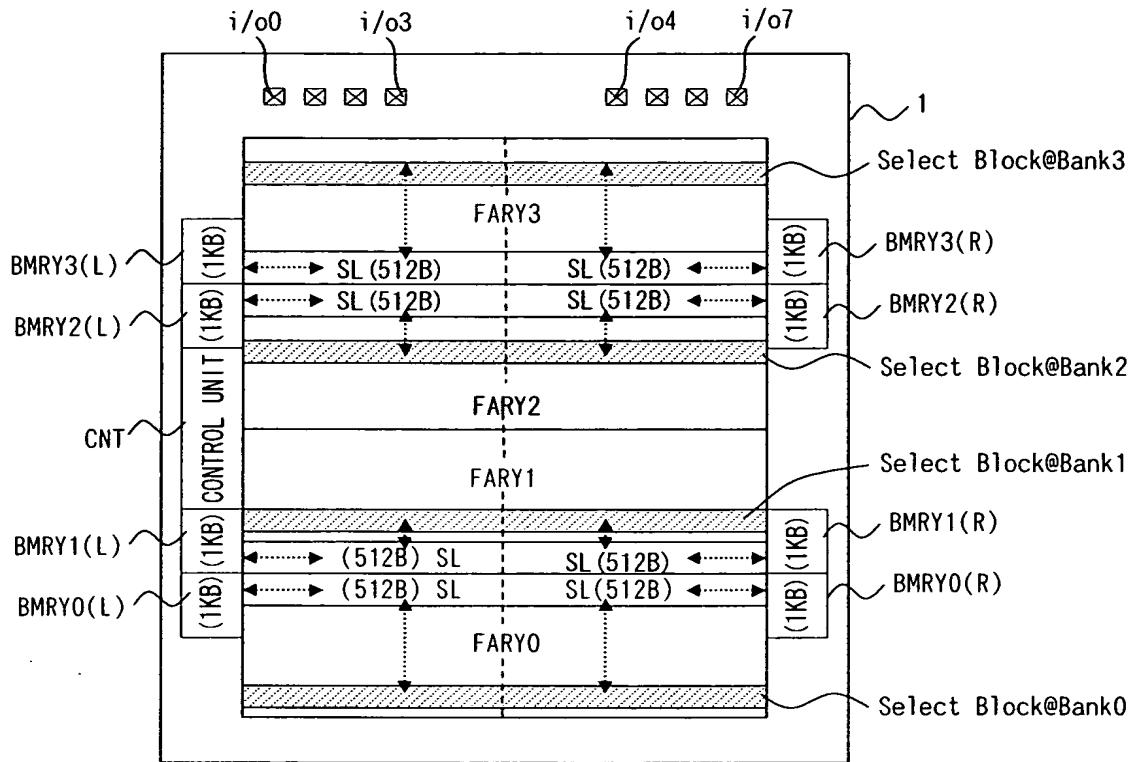


FIG.5

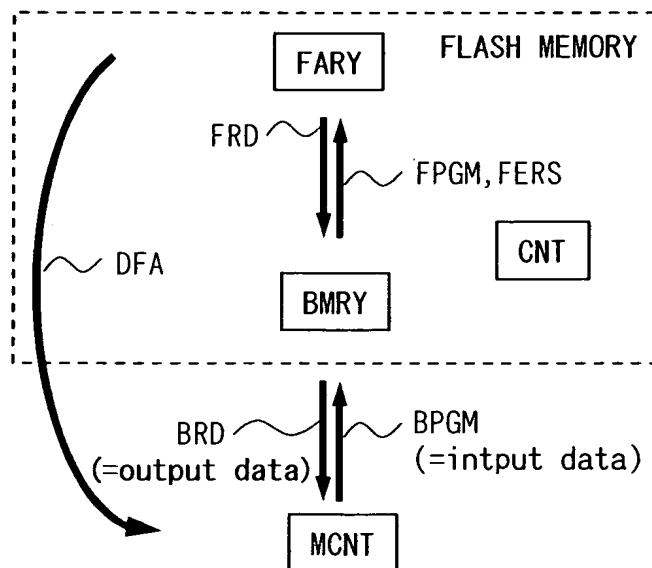
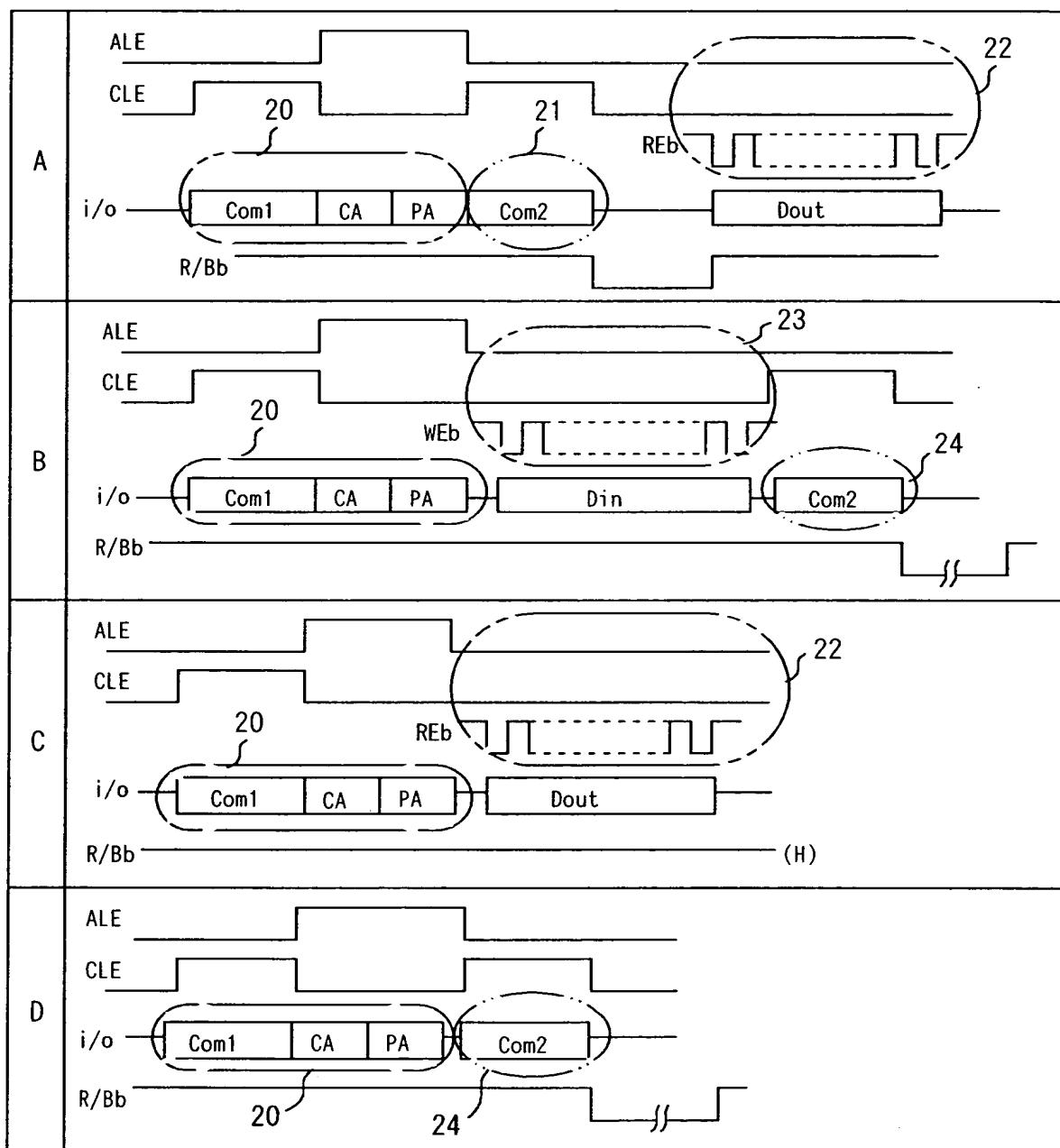


FIG.6



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FIG.7

No.	Com1	CA	PA	Com2	BMPY CLEAR	TYPE	SUMMARY	
							Read 4 continuous pages (4 pages at maximum)	Set CA in read (the optional number of times can be input)
1	00H	○	○	30H	×	Continuous 4-page (old sector) read	Set CA and PA in read (the optional number of times can be input)	Set CA and PA in read (the optional number of times can be input)
2	05H	○	×	E0H	×	Set CA for Random Output in a page	Set CA and PA in read (the optional number of times can be input)	Set CA and PA in read (the optional number of times can be input)
3	06H	○	○	E1H	×	Set CA and PA for Random Output	Set up a write	Set up a write
4	80H	○	○	11H	○	Write set-up	Write for a bank set with 4 and 5 (simple upper end)	Write for a bank set with 4 and 5 (simple upper end)
5	80H	○	○	10H	○	Write	Write 4 continuous pages	Write 4 continuous pages
6	85H	○	×			Set CA for Random Input	Read 1 page for 1 bank	Read 1 page for 1 bank
7	00H	○	○	35H		Read for a copy pack	Write data read to BMPY	Open BMPY which is identical to No.5 and is not used in a write
8	85H	○	○	10H		Write for a copy pack	Open BMPY which is identical to No.5 and is not used in a write	Open BMPY which is identical to No.5 and is not used in a write
9	80H	○	○	15H	○	Cache program	Erase 1 block for 1 bank	Erase 1 block for 1 bank
10	60H	×	○	D0H		Block erase	Read a status code (70H to 76H)	Read a status code (70H to 76H)
11	7*H					Status read	Read an ID code	Read an ID code
12	90H					ID read	Reset an internal state and clear a status register	Reset an internal state and clear a status register
13	FFH					Forcible reset	Read to	Read to
14	00H	○	○	31H		Different bank read	Write to (simple upper end)	Write to (simple upper end)
15	80H	○	○	12H		Different bank write	Write data for 4 continuous pages	Write data for 4 continuous pages
16	80H	○	○	13H		Continuous 4-page write	Write having No. 5 preverify	Write having No. 5 preverify
17	80H	○	○	16H	○	Write 2	Write having No. 15 preverify	Write having No. 15 preverify
18	80H	○	○	17H		Different bank write 2	Write having No. 16 preverify	Write having No. 16 preverify
19	80H	○	○	18H		Continuous 4-page write 2	Read in a binary mode	Read in a binary mode
20	00H	○	○	3AH		Binary read	Erase 4 continuous blocks	Erase 4 continuous blocks
21	60H	○		D1H		Continuous 4-block erase	Clear all BMPY buffers	Clear all BMPY buffers
22	FEH					Buffer clear		

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FIG. 8

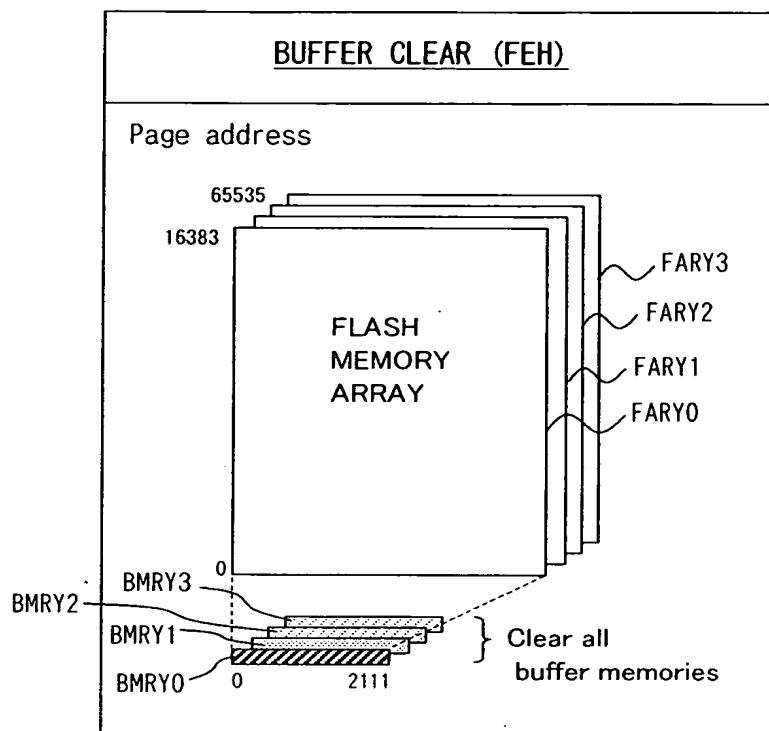


FIG. 9

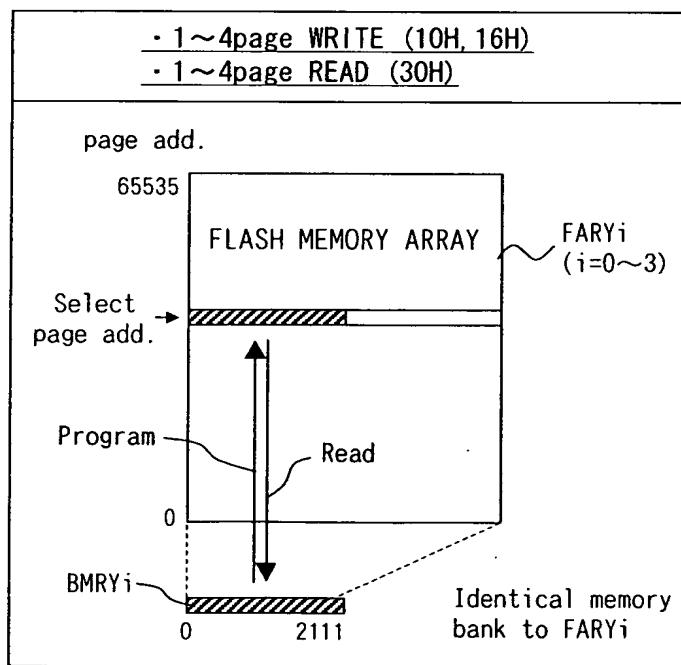


FIG.10

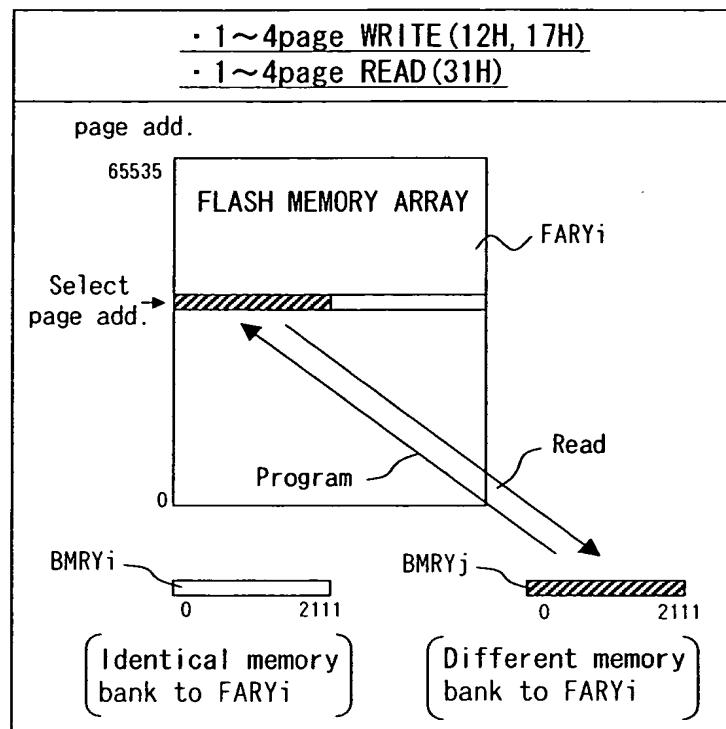
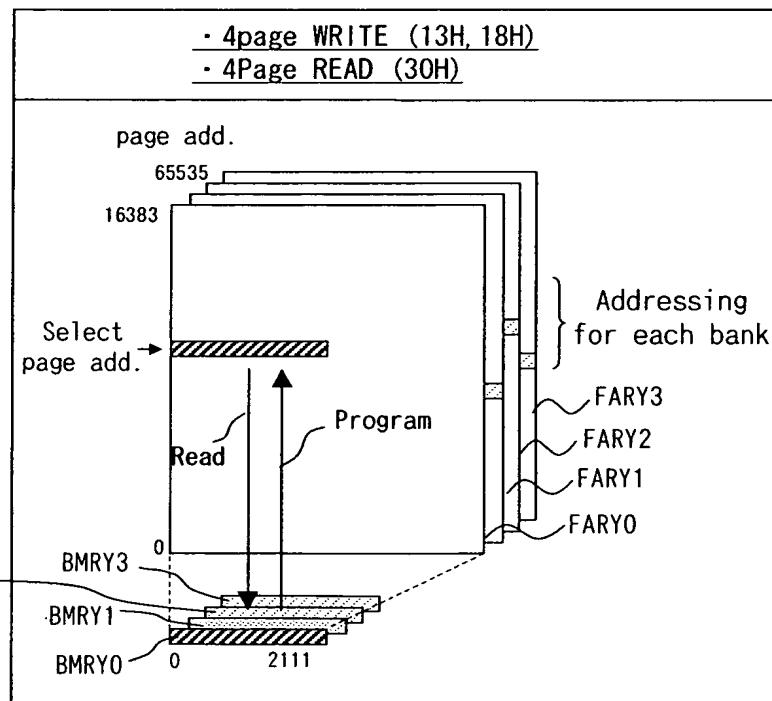


FIG.11



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FIG.12

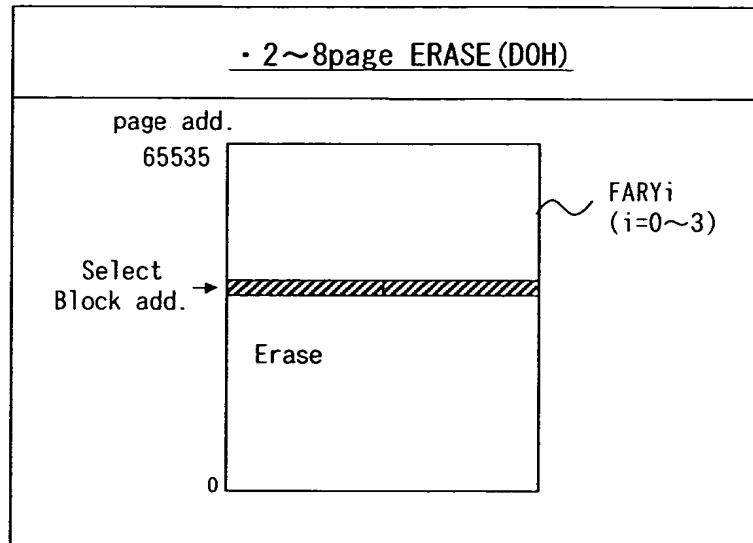
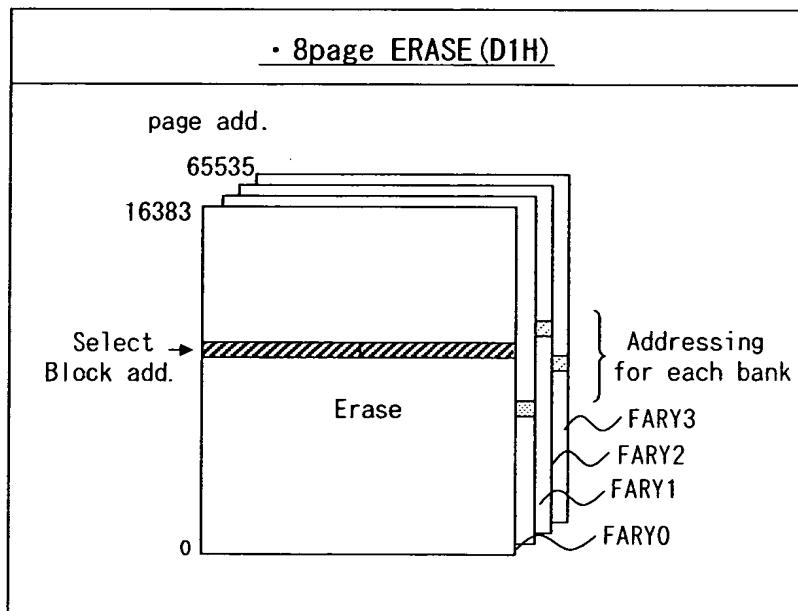


FIG.13



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FIG.14

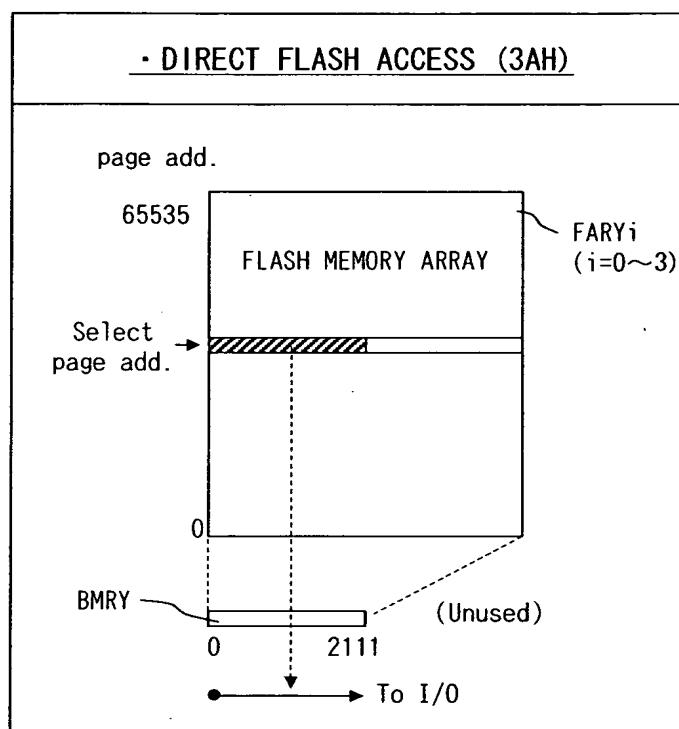
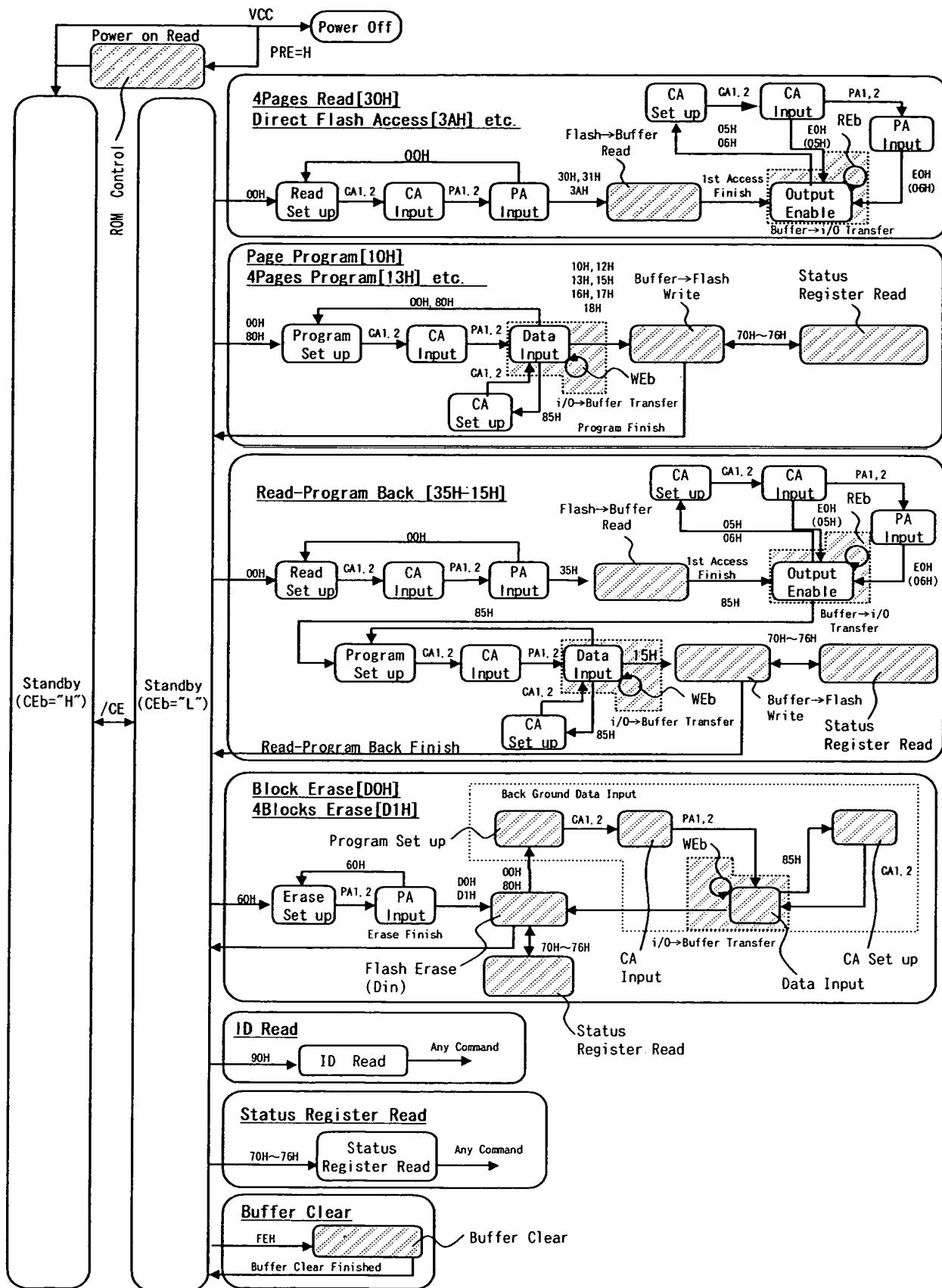


FIG.15



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FIG.16

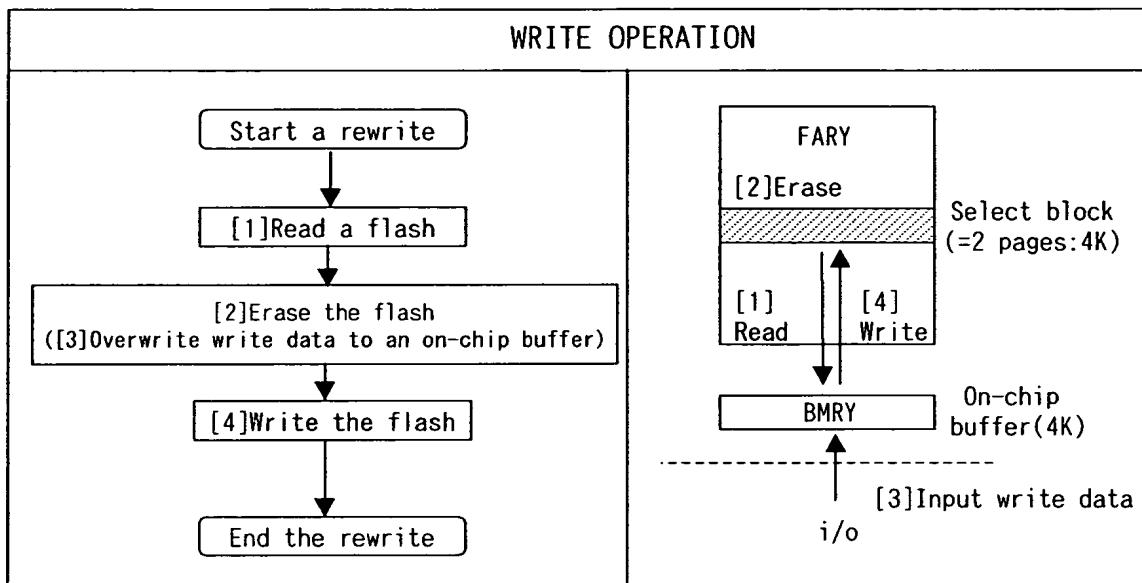


FIG.17

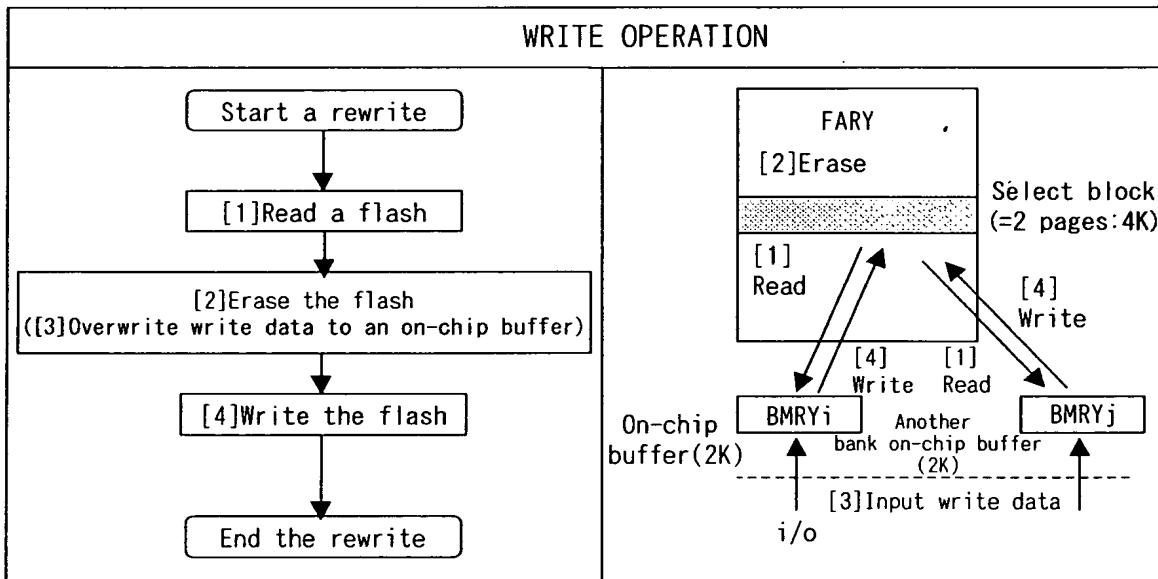


FIG.18

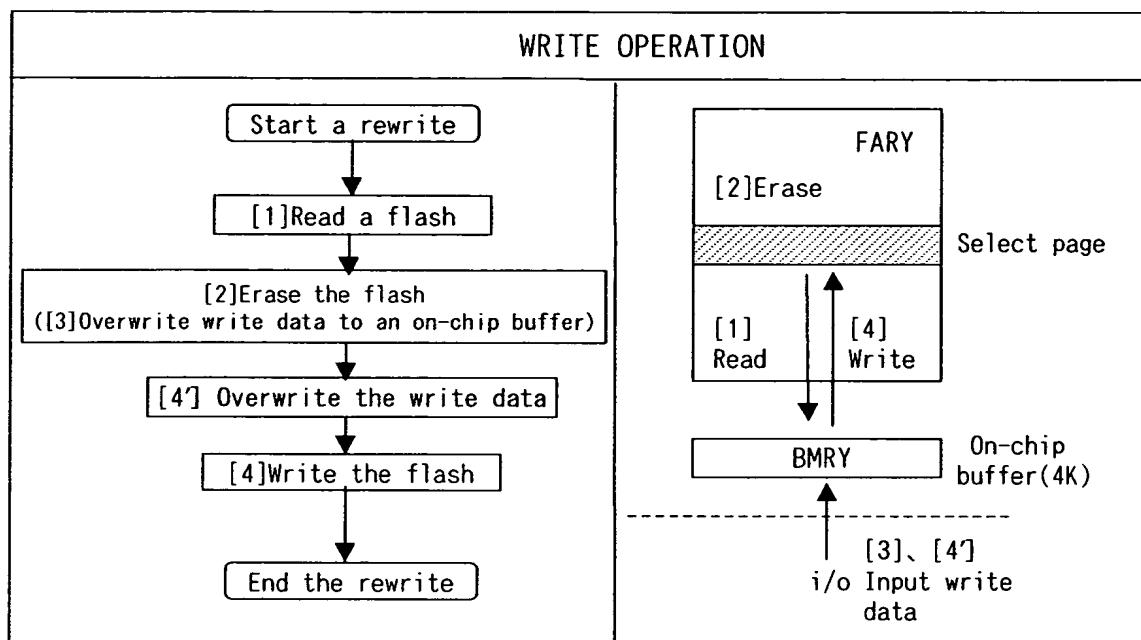


FIG.19

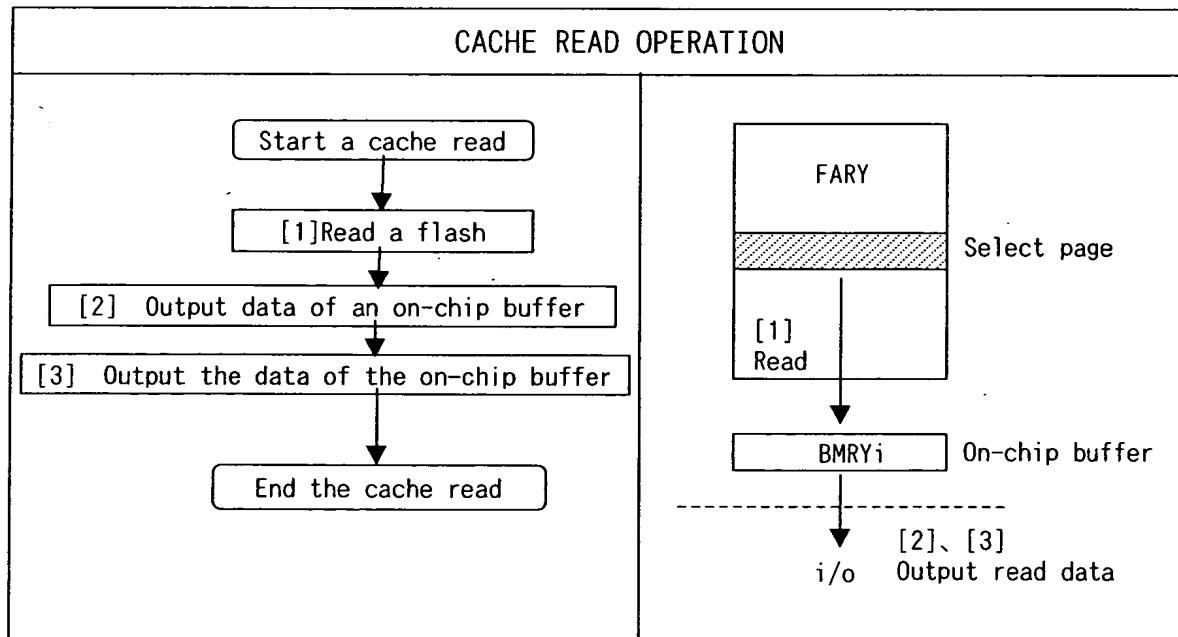


FIG.20

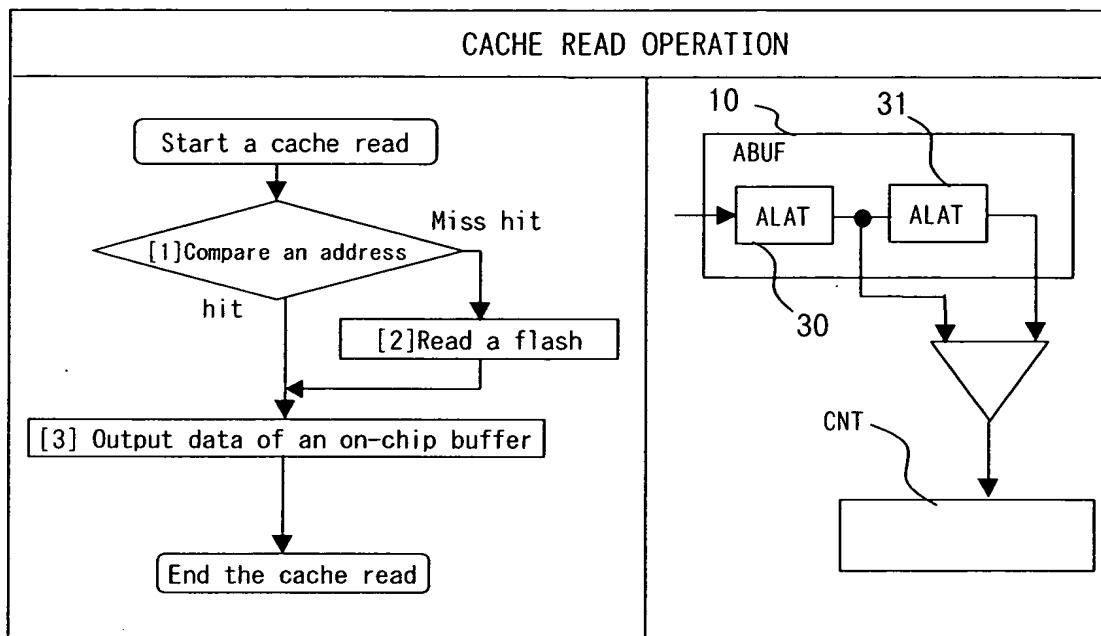
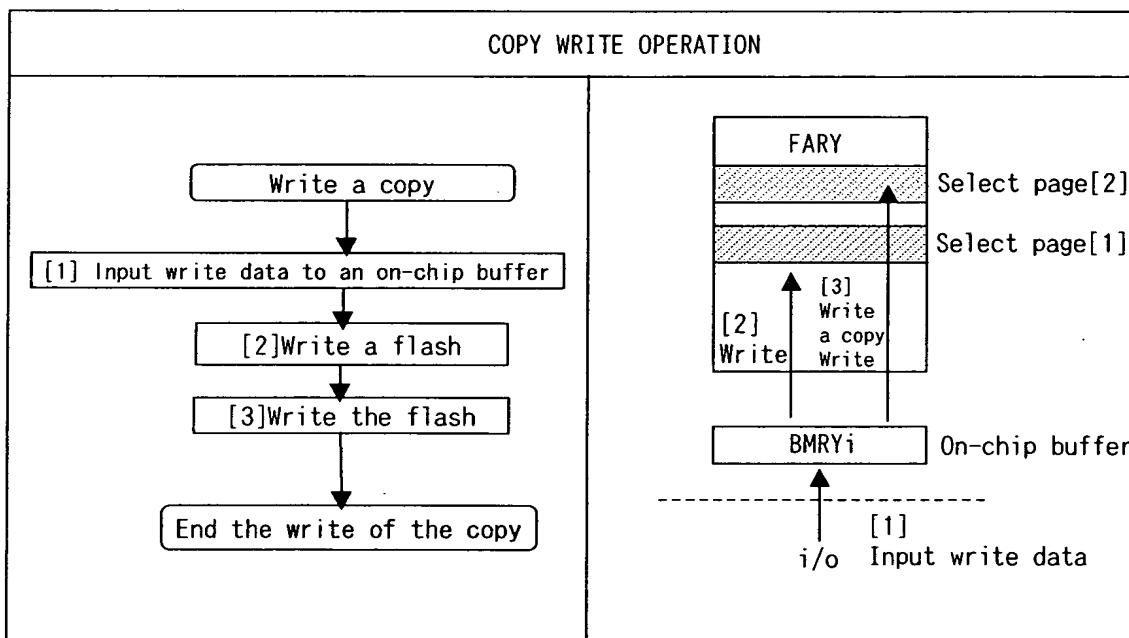
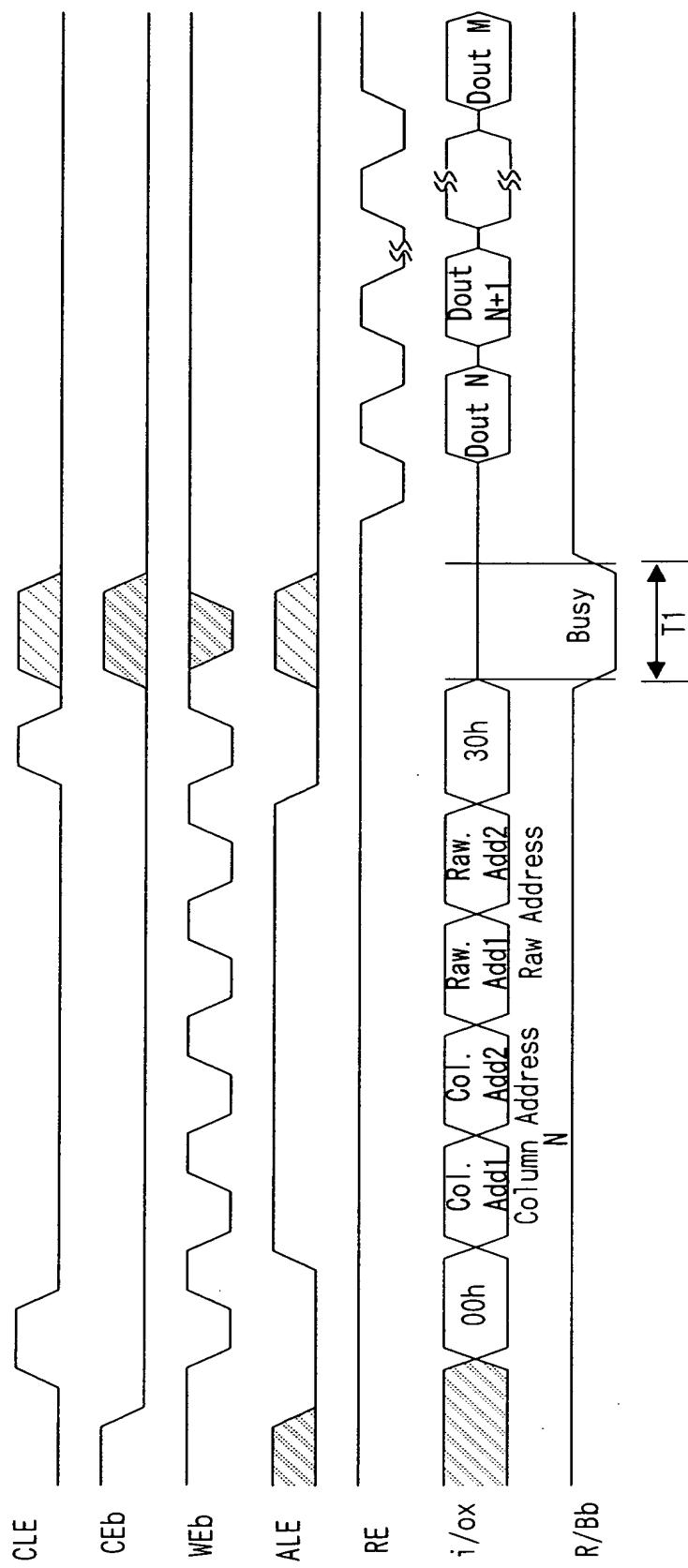


FIG.21



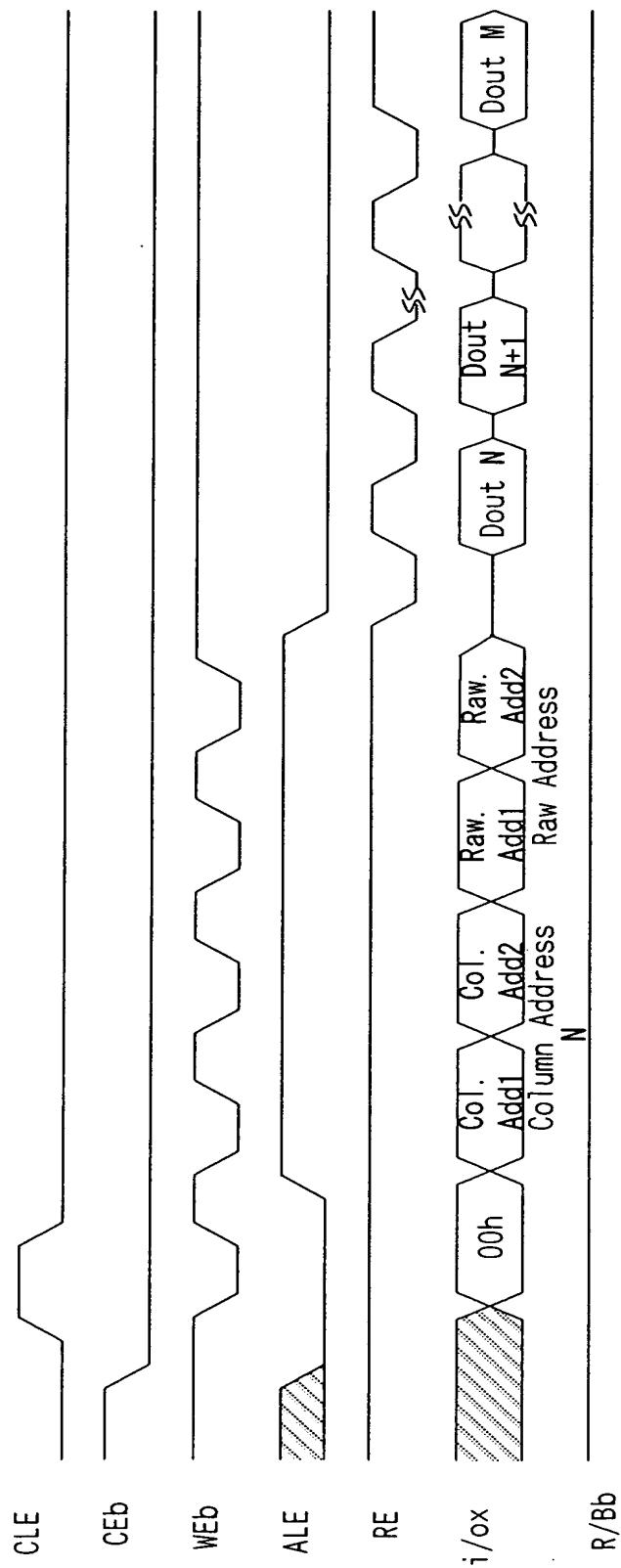
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FIG.22



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FIG.23



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FIG.24

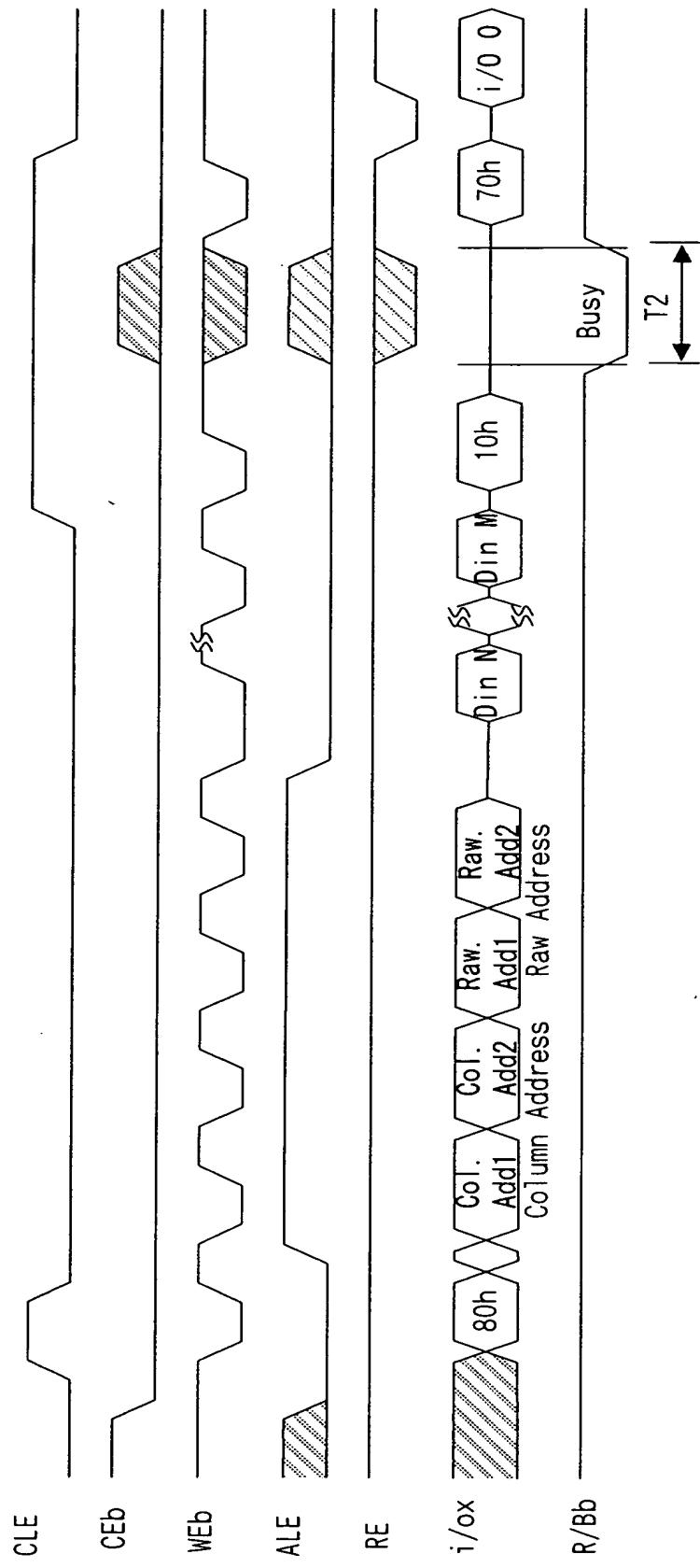


FIG.25

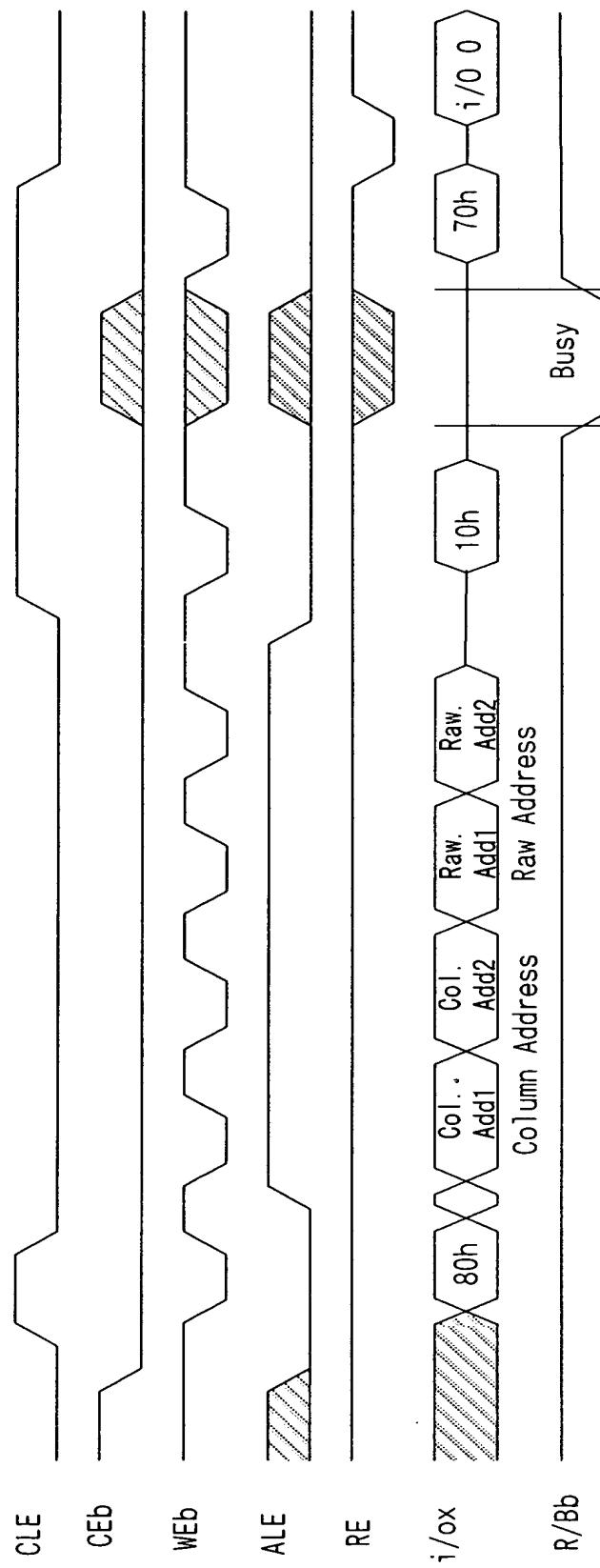


FIG. 26

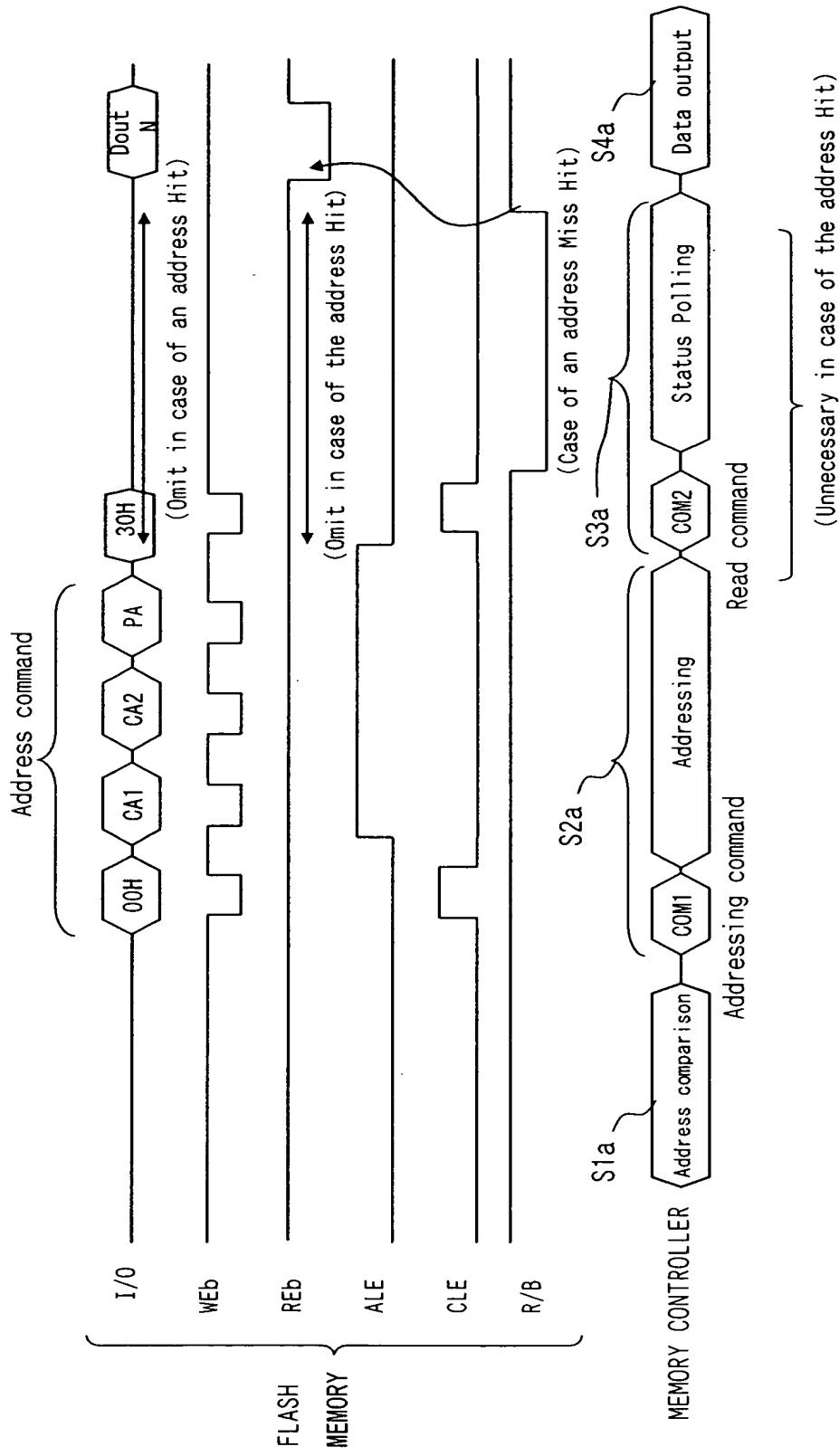


FIG.27

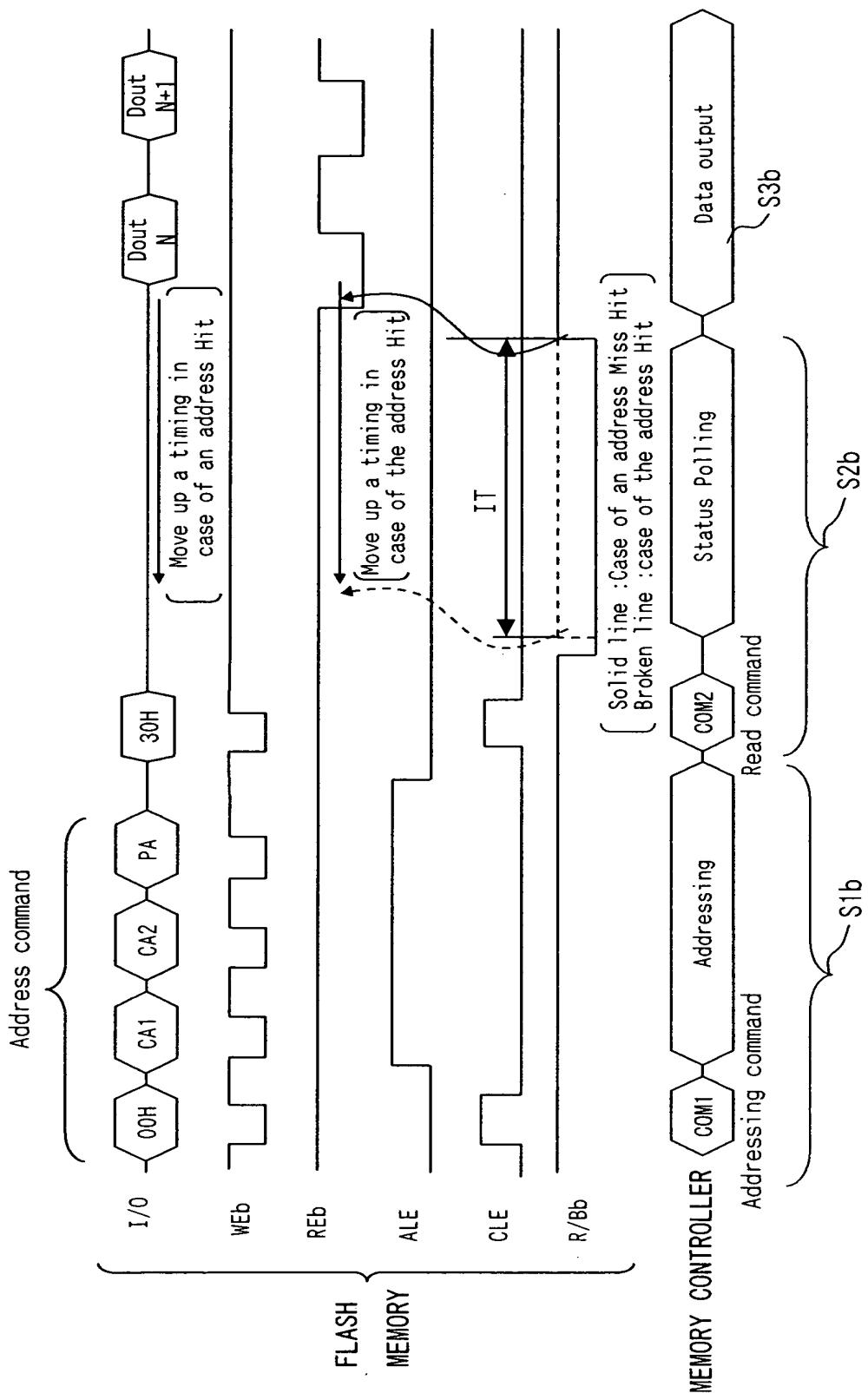


FIG.28

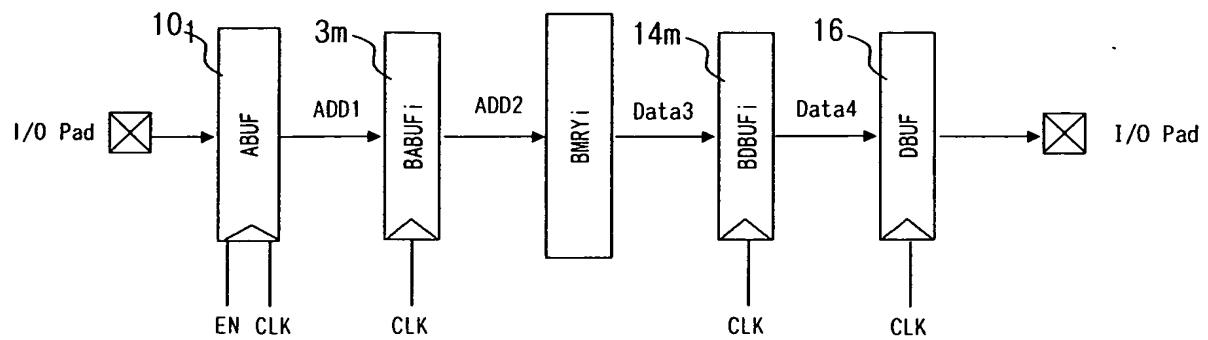
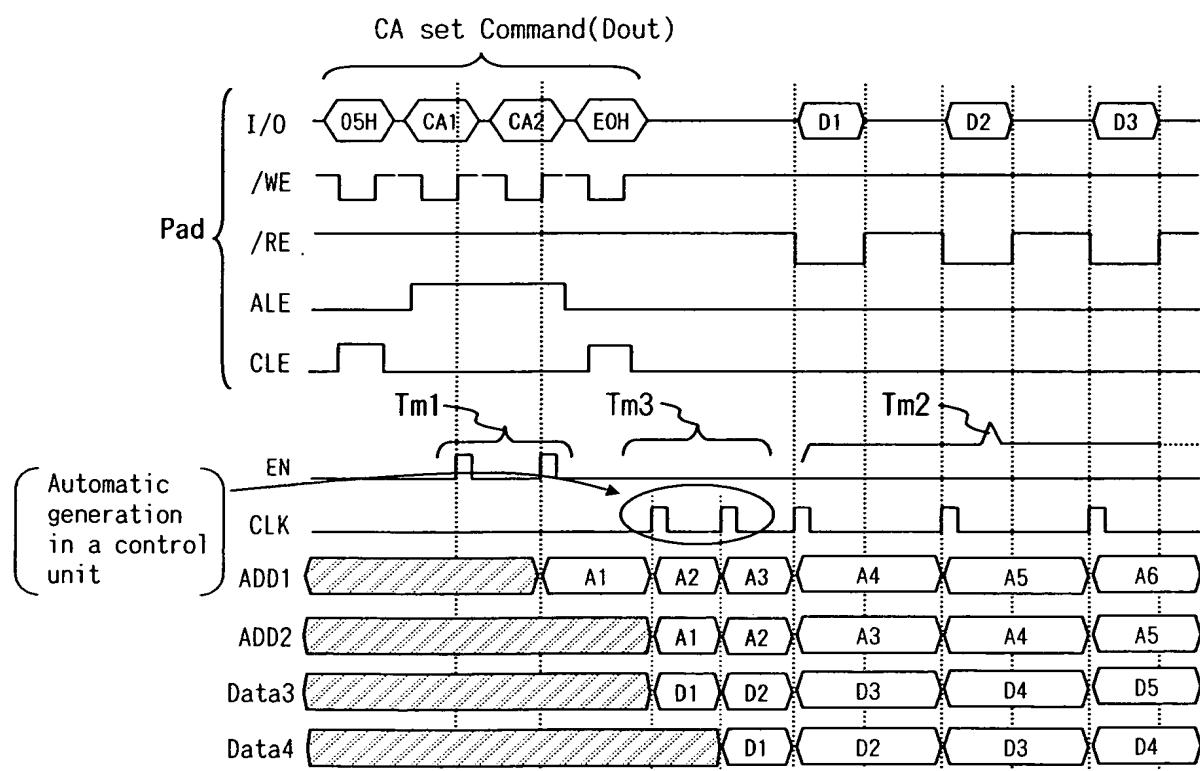


FIG.29



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FIG.30

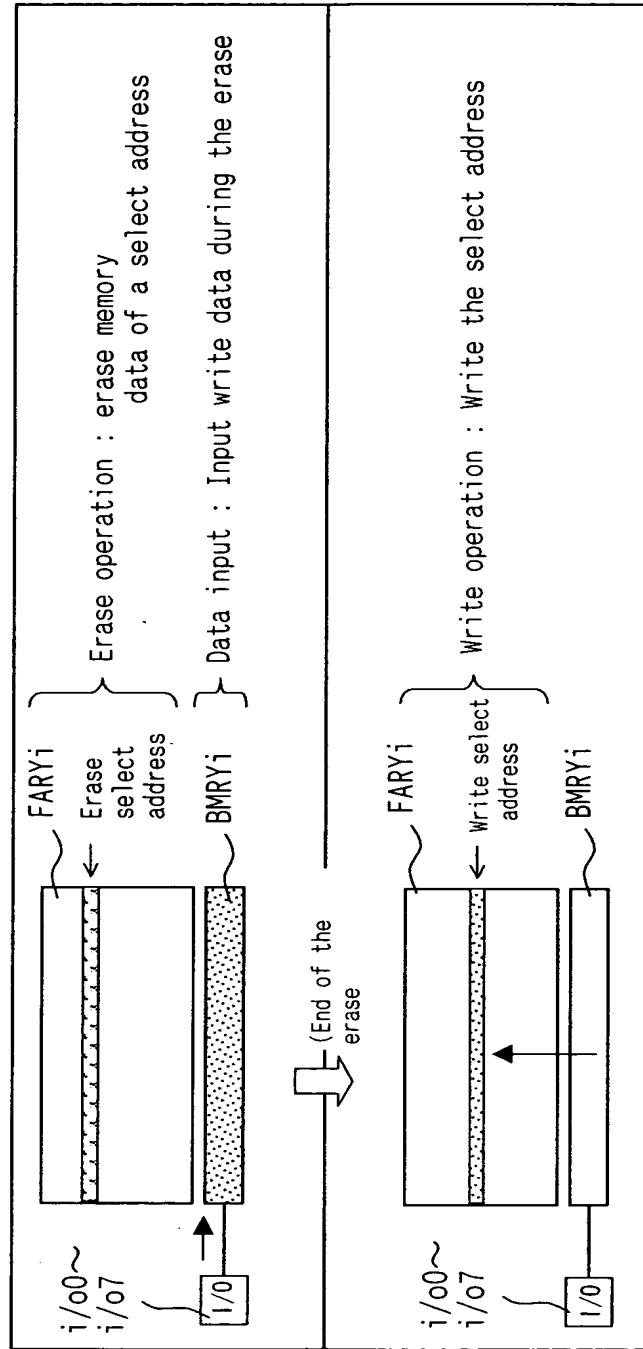


FIG.31

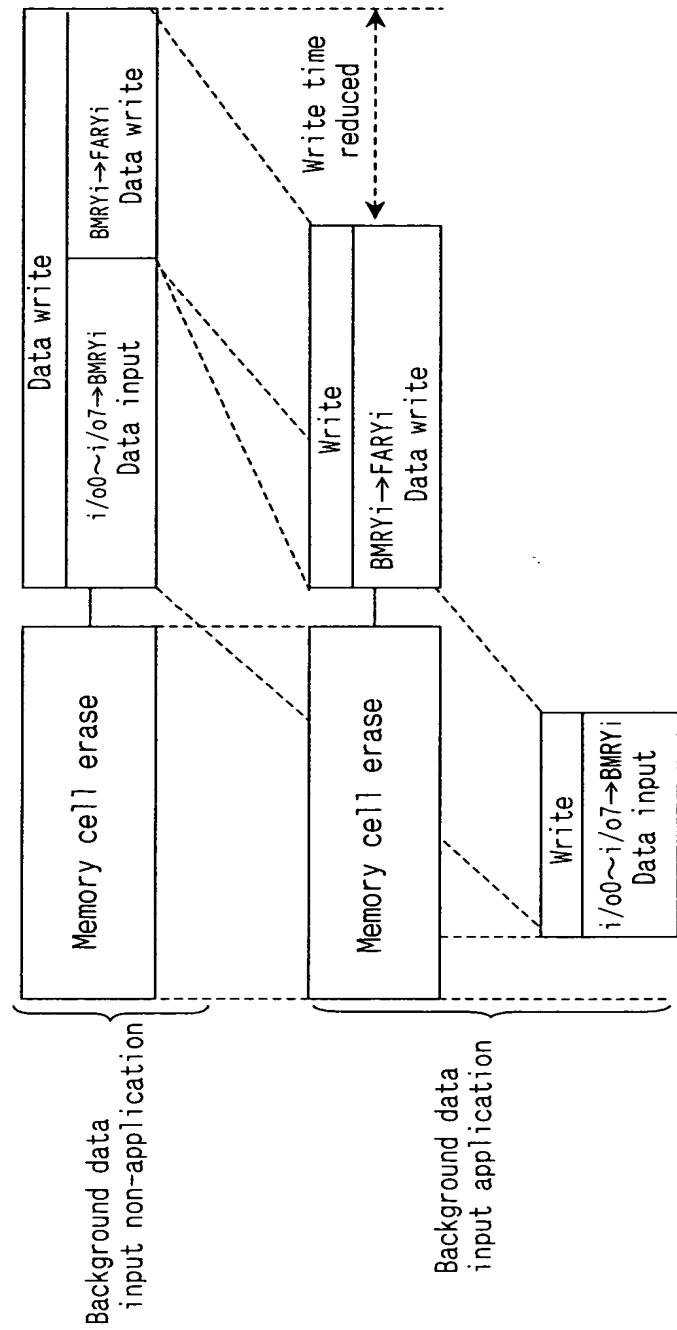
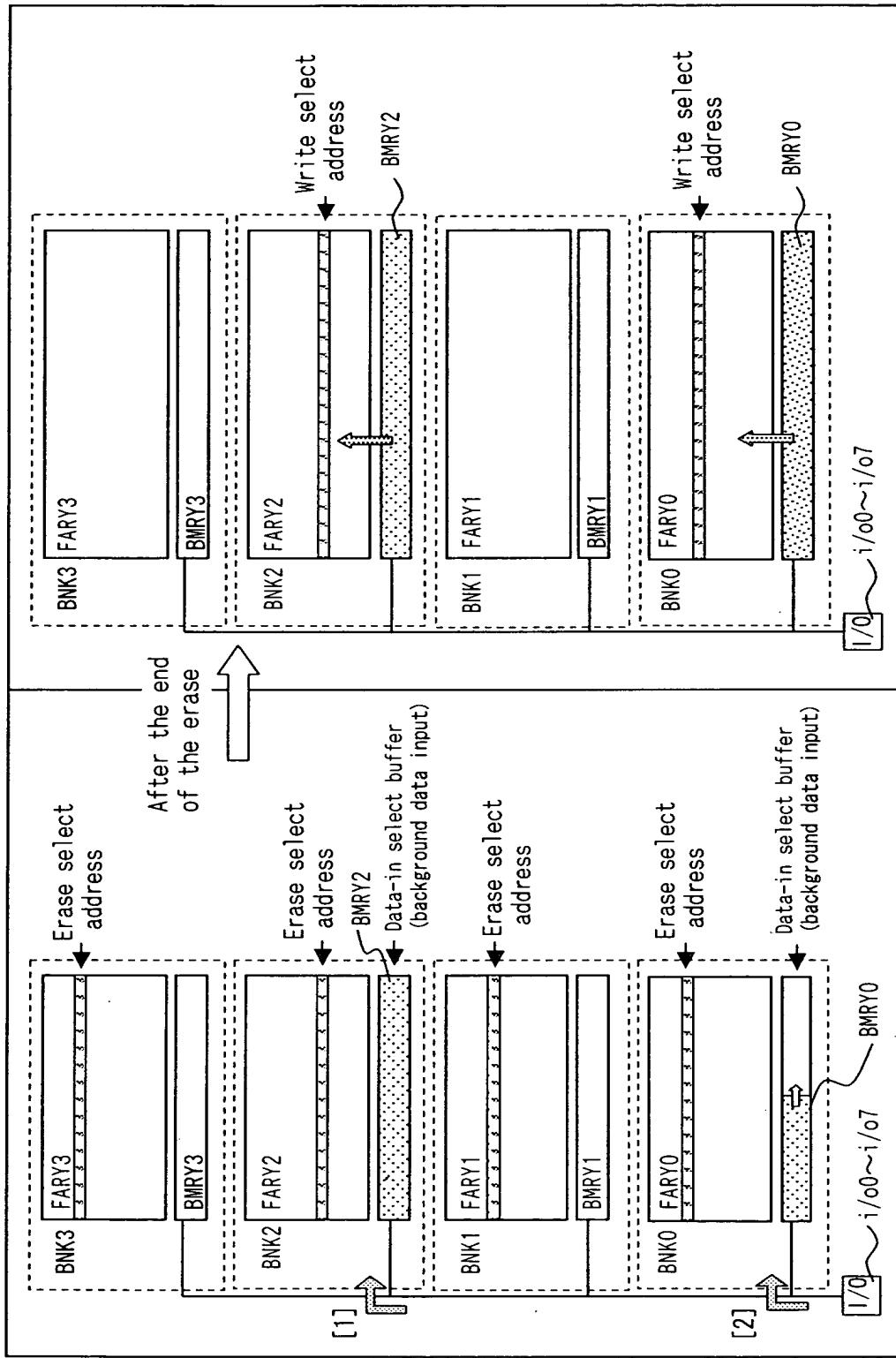


FIG.32



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FIG. 33

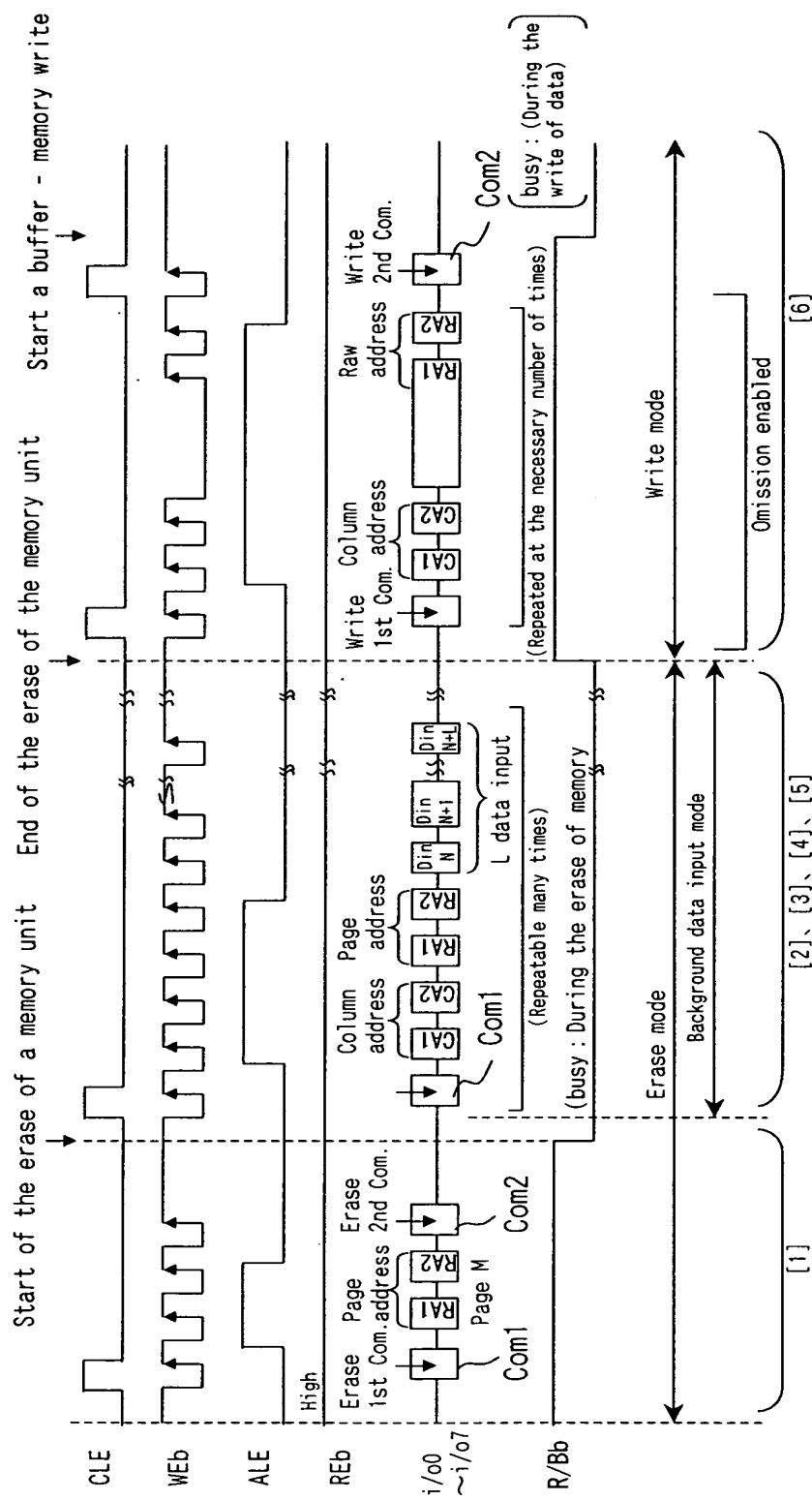


FIG.34

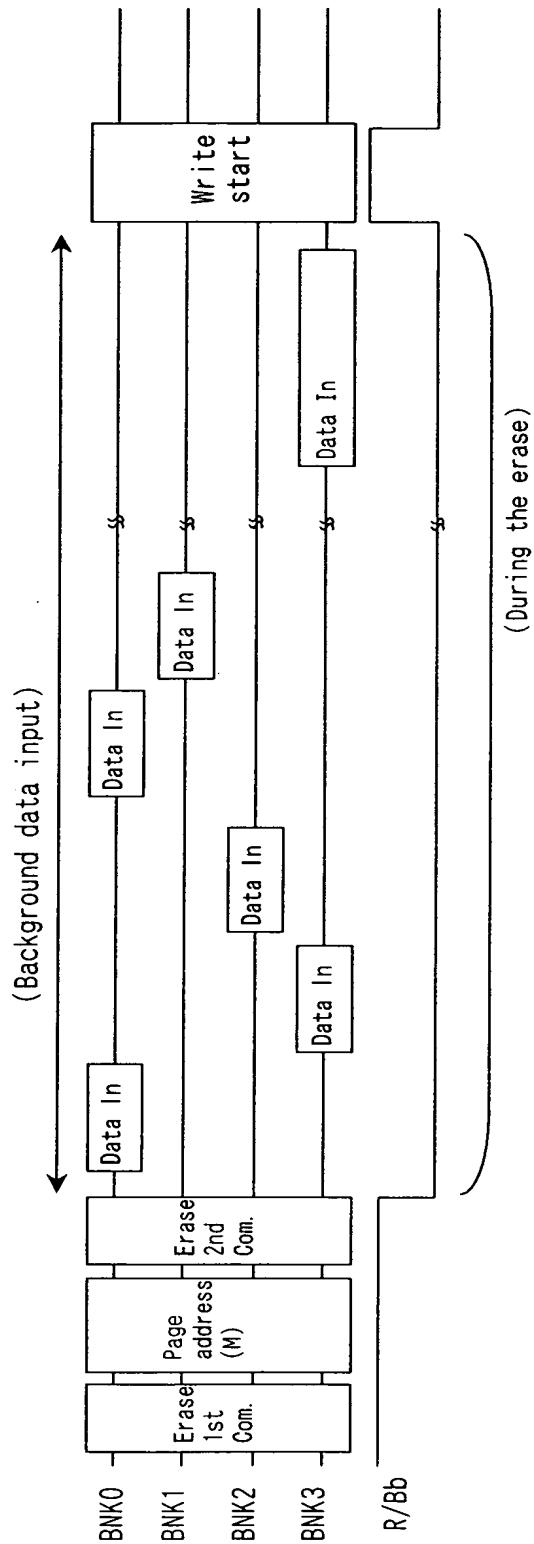


FIG.35

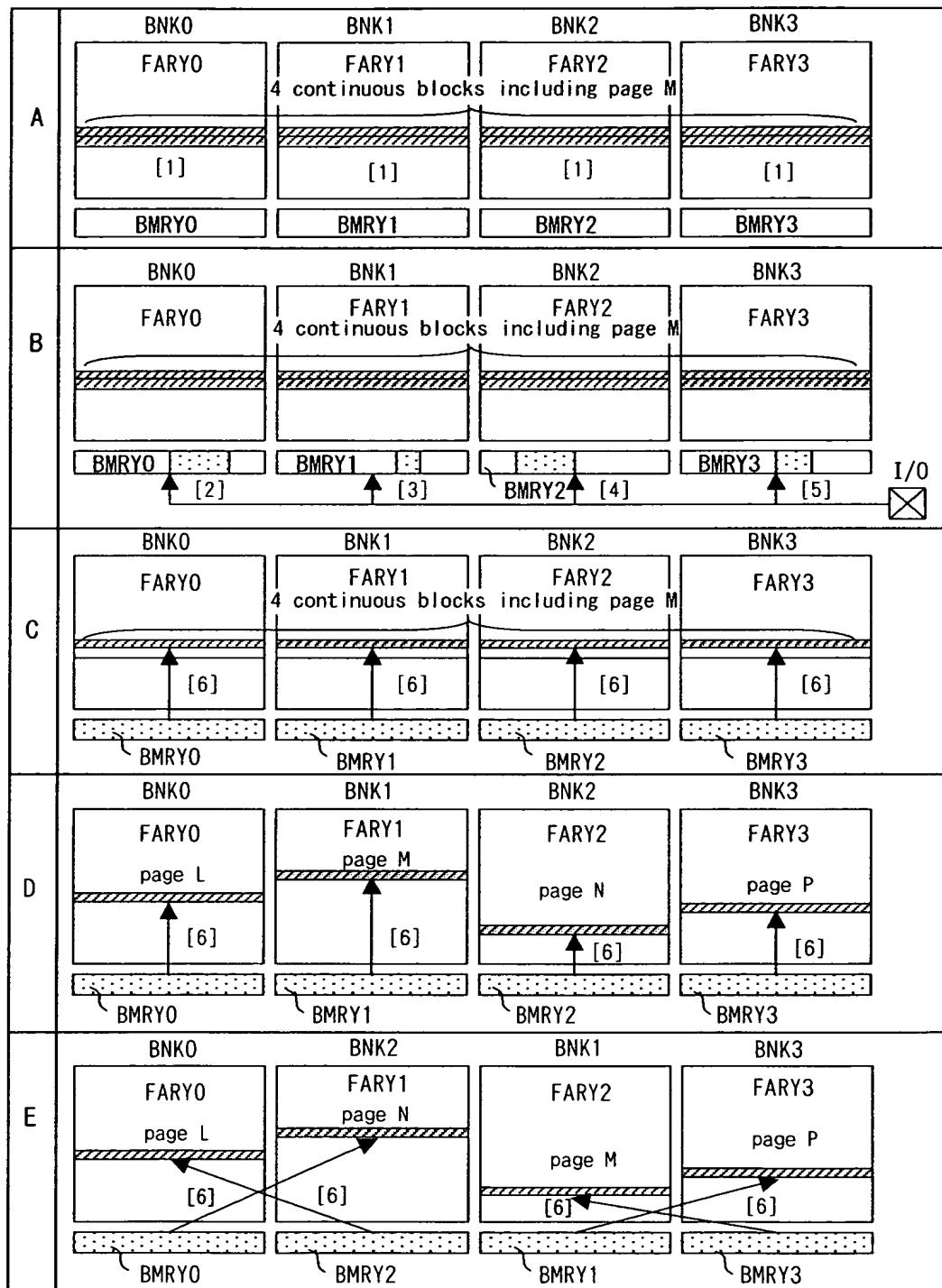
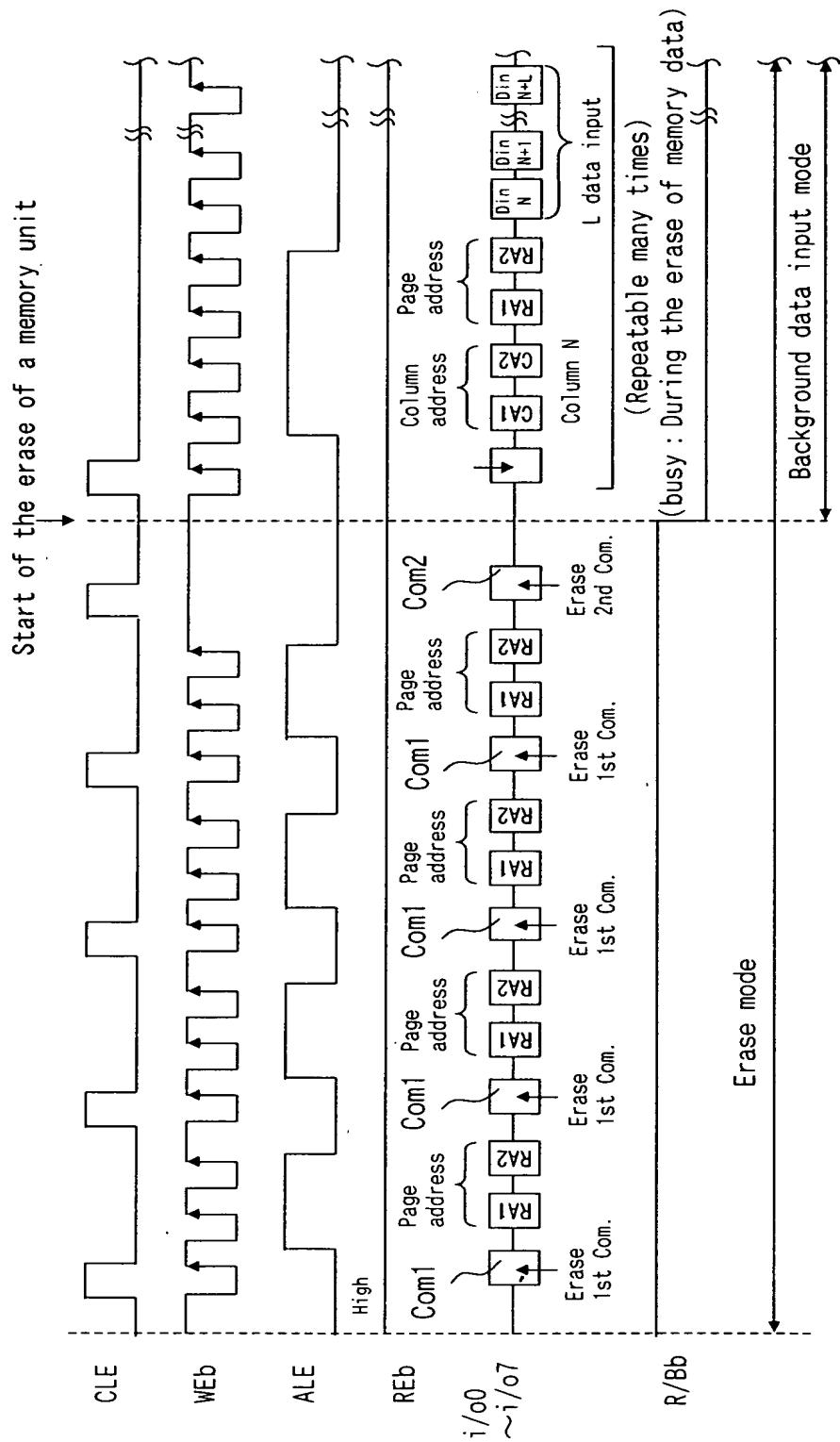
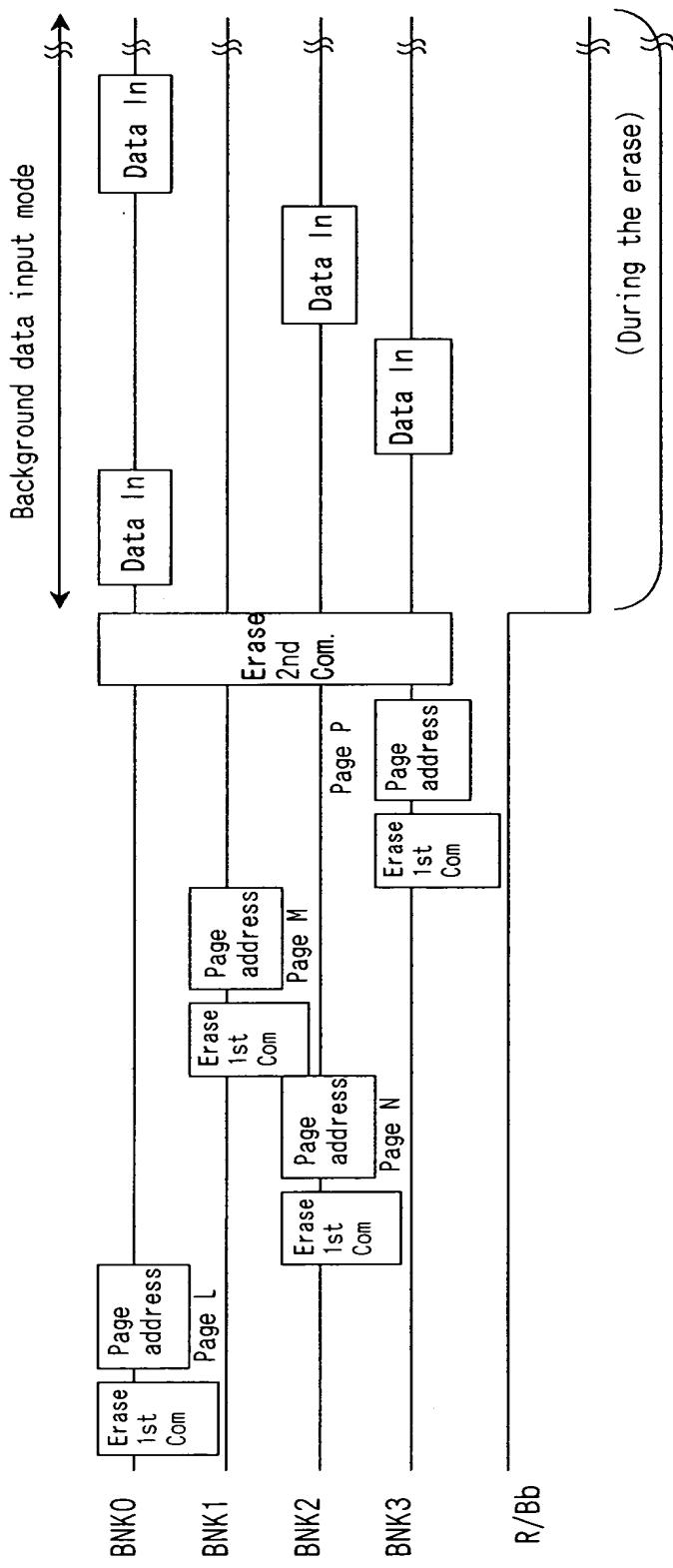


FIG. 36



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FIG.37



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FIG.38

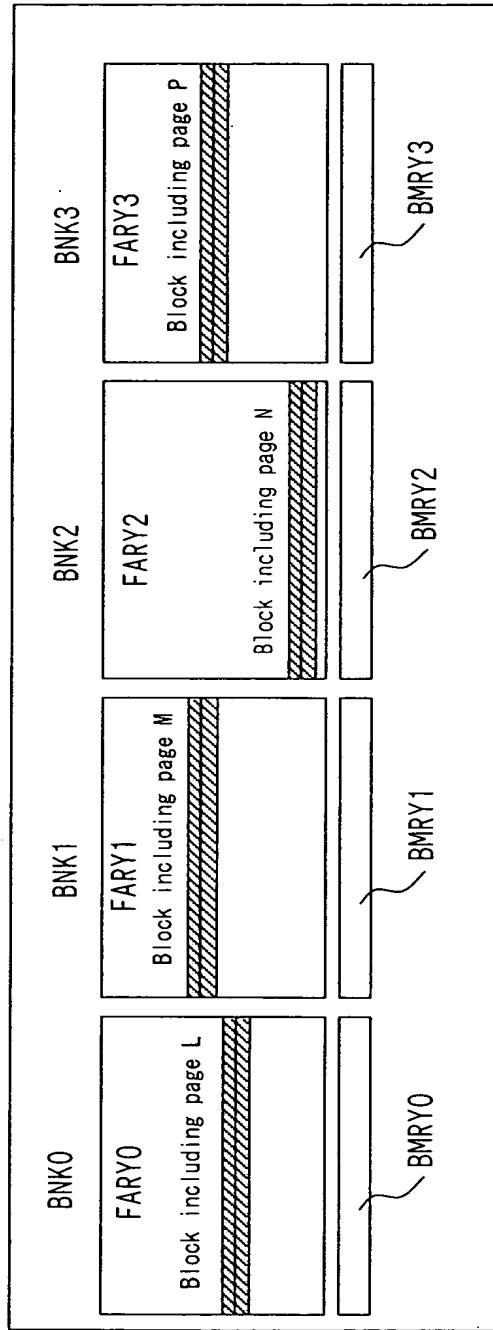
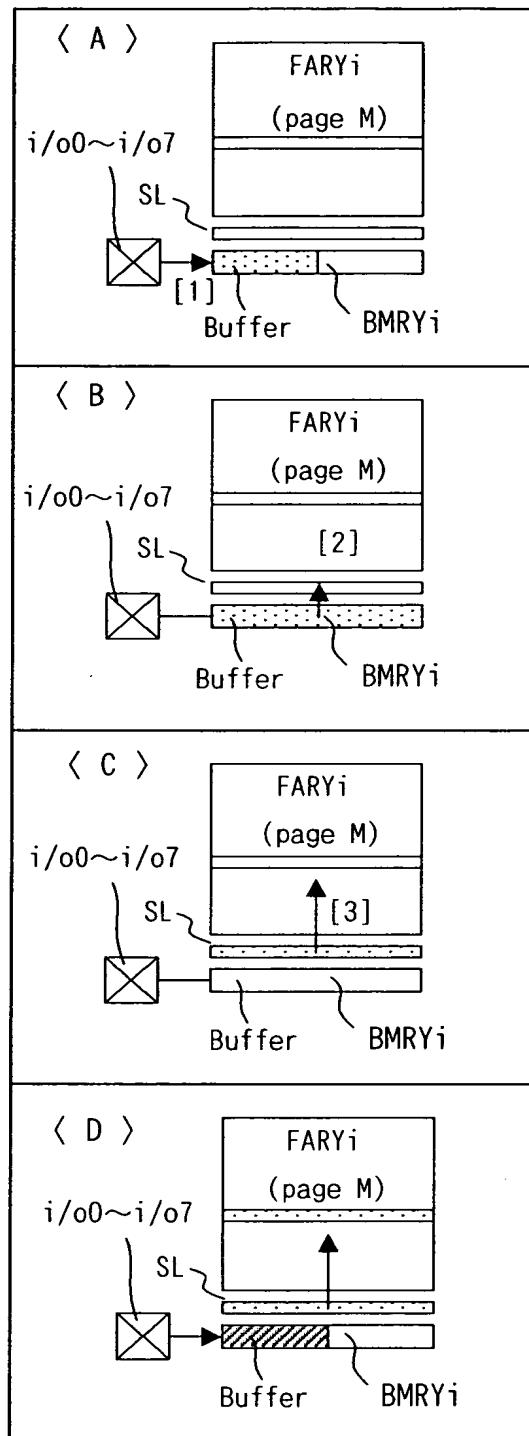


FIG.39



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FIG.40

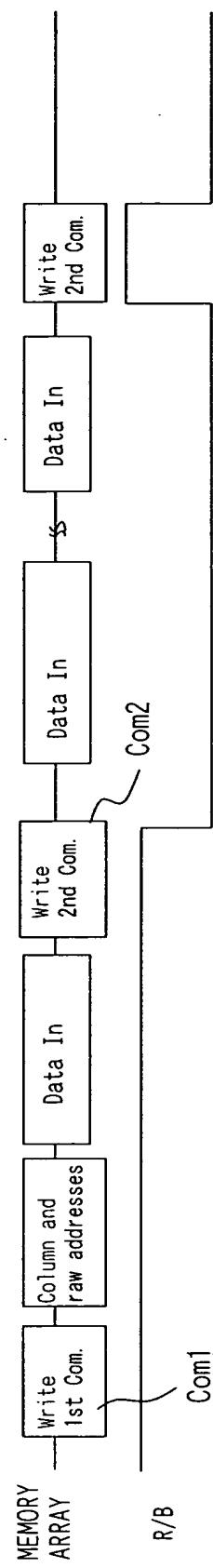


FIG.41

